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PACKET OUT OF DISTRIBUTION CHIP

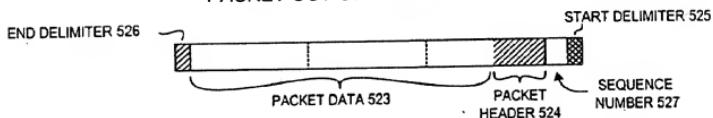
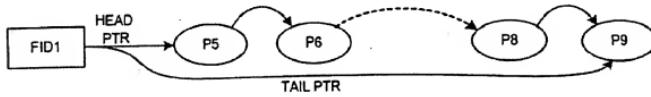
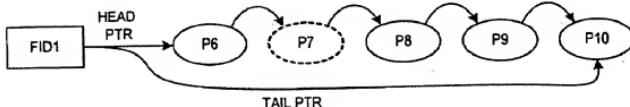


FIG. 49



PACKET QUEUE

FIG. 50



PACKET QUEUE

FIG. 51

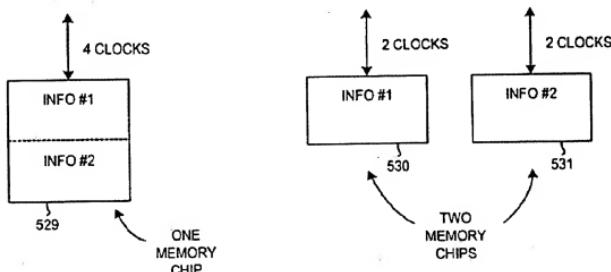


FIG. 52